

## **Research**

### **2025-2026**

1. Ms. P. Lavanya has successfully defended her thesis during her Ph.D. Viva Voce examination and awarded Ph.D. degree on 05.09.2025.
2. Mr. P. Venugopal, HOD/ECE and Mr. T. Sasikala, Prof /ECE has published a paper titled “An area and power efficient VLSI architecture for epileptic seizure detection using Transpose Form Retimed Delayed LMS filter and spiking neural networks” in Integration, the VLSI Journal during July 2025.